

WHAT IS CLAIMED IS:

1. A laminated electronic component comprising:
a laminated block including a plurality of electrically insulating layers and an internal conductor film disposed between the insulating layers laminated together in a thickness direction of said laminated block;
an external conductor film disposed on an exposed surface of said laminated block; and
an additional conductor film which is at the same electric potential as said external conductor film and which is arranged along a specific interface between the insulating layers such that said additional conductor film faces said external conductor film.
2. A laminated electronic component according to claim 1, wherein only one of the insulating layers is interposed between said additional conductor film and said external conductor film.
3. A laminated electronic component according to claim 1, wherein the thickness of the insulating layer between said additional conductor film and said external conductor film ranges from about 10 μm to about 150 μm .
4. A laminated electronic component according to claim 1, wherein the thickness of the insulating layer

between said additional conductor film and said external conductor film ranges from about 25 μm to about 150 μm .

5. A laminated electronic component according to claim 1, wherein the area of said additional conductor film is greater than or equivalent to the area of said external conductor film, and is arranged such that said additional conductor film covers said external conductor film therein when viewed from above or below.

6. A laminated electronic component according to claim 1, wherein said additional conductor film and said external conductor film are electrically connected to each other through a via-hole conductor.

7. A laminated electronic component according to claim 1, wherein said additional conductor film and said external conductor film are electrically connected to each other through a conductor disposed on an outer surface of said laminated block.

8. A laminated electronic component according to claim 1, wherein a DC bias is applied between said external conductor film and said internal conductor film.

9. A laminated electronic component according to claim 1, wherein said laminated block includes a first main surface and a second main surface facing the first

main surface, and said external conductor film is disposed on at least one of the first and second main surfaces.

10. A laminated electronic component according to claim 9, further comprising a chip component mounted on at least one of the first and second main surfaces, wherein said external conductor film is arranged to establish an electrical connection with said chip component.

11. A laminated electronic component according to claim 10, wherein said chip component is one of a capacitor, an inductor, a resistor, a diode, an integrated circuit, a memory device, a SAW filter and a quartz oscillator.

11. A laminated electronic component according to claim 9, wherein said external conductor film is arranged to establish an electrical connection with a board on which said laminated electronic component is mounted.

12. A laminated electronic component according to claim 1, wherein a cavity having an opening positioned on at least one of main surfaces of said laminated block is provided in said laminated block, and said external conductor film is disposed on the bottom surface of the cavity.

13. A laminated electronic component according to claim 12, further comprising a chip component housed in

the cavity, wherein said external conductor film defines a die bonding surface for bonding the chip component.

14. A laminated electronic component according to claim 12, wherein said additional conductor film and said external conductor film are electrically connected to each other through a via-hole conductor, and the via-hole conductor is positioned in an area outside the bottom surface of the cavity.

15. A laminated electronic component according to claim 1, wherein said insulating layers are formed of ceramic material.

16. A laminated electronic component according to claim 1, wherein said internal conductor film defines at least one of a capacitor, a ground potential and a wiring for connection to an electronic component.

17. A laminated electronic component according to claim 1, further comprising a plurality of internal conductors and via-hole conductors which are arranged to provide at least one of wiring patterns, capacitors, inductors, delay lines, and filters.

18. A laminated electronic component according to claim 17, wherein the plurality of internal conductors and via hole conductors are disposed within said laminated block.

19. A laminated electronic component according to claim 1, further comprising resistor films for defining resistors.

20. A laminated electronic component according to claim 19, wherein said resistor films are disposed within said laminated block.